

CAPITÓLIO

MODULE TYPE		CSST42
Thyristor Modules		$I_{TRMS} = 2x 80 A$ $I_{TAVM} = 2x 51A$ $V_{RRM} = 800-2300 V$
VOLTAGE RATINGS MAXIMUM	LIMITS UNITS	
V_{DRM} Repetitive peak off-state voltage,	1600-2200 V	
V_{DSM} Non-repetitive peak off-state voltage,	1600-2200 V	
V_{RRM} Repetitive peak reverse voltage,	1600-2200 V	
V_{RSM} Non-repetitive peak reverse voltage	1700-2300V	

OTHER RATINGS MAXIMUM LIMITS UNITS

Symbol	Test Conditions	Maximum	Ratings
I_{FRMS}	$T_{VJ} = T_{VJM}$	80	A
I_{FAVM}	$T_C = 100^{\circ}C$; 180° sine	51	A
I_{FSM}	$T_{VJ} = 45^{\circ}C$; t = 10 ms (50 Hz), sine	1150	A
I_{FSM}	$V_R = 0$ t = 8.3 ms (60 Hz), sine	1230	A
I_{FSM}	$T_{VJ} = T_{VJM}$ t = 10 ms (50 Hz), sine	1000	A
I_{FSM}	$V_R = 0$ t = 8.3 ms (60 Hz), sine	1070	A
$\int i_2 dt$	$T_{VJ} = 45^{\circ}C$ t = 10 ms (50 Hz), sine	6600	A ² s
$\int i_2 dt$	$V_R = 0$ t = 8.3 ms (60 Hz), sine	6280	A ² s
$\int i_2 dt$	$T_{VJ} = T_{VJM}$ t = 10 ms (50 Hz), sine	5000	A ² s
$\int i_2 dt$	$V_R = 0$ t = 8.3 ms (60 Hz), sine	4750	A ² s
$(di/dt)_{cr}$	$T_{VJ} = T_{VJM}$ repetitive, $I_T = 150 A$ f = 50 Hz, $t_P = 200 \mu s$ $V_D = 2/3 V_{DRM}$	150 A/ μs	A/ μs
	$I_G = 0.45 A$ non repetitive, $I_T = I_{TAVM}$ $di_G/dt = 0.45 A/\mu s$	500 A/ μs	A/ μs
$(dv/dt)_{cr}$	$T_{VJ} = T_{VJM}$; $V_{DR} = 2/3 V_{DRM}$ $R_{GK} = \infty$; method 1	1000	V/ μs
P_{GM}	$T_{VJ} = T_{VJM}$ $t_P = 30 \mu s$ $I_T = I_{TAVM}$ $t_P = 300 \mu s$	10 5 0.5	W W W
P_{GAV}			
V_{RGM}		10	V
T_{VJ}	repetitive, $I_T = 250 A$	-40...+150	°C
T_{VJM}		125	°C
T_{stg}		-40...+150	°C
V_{ISOL}	50/60 Hz, RMS t = 1 min	3000	V~
V_{ISOL}	$I_{ISOL} = 1 mA$ t = 1 s	3600	V~
M_d	Mounting torque (M5)	2.5-4.0/ 22-35	Nm/lb.in.
M_d	Terminal connection torque (M6)	2.5-4.0/ 22-35	Nm/lb.in.
Weight	Typical including screws	90	g

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Dimensions in mm (1 mm = 0.0394")

